

**PATENT**  
**(5500-36101/TT2823CPA)**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 09/207,972  
Filed: 12/9/98  
Inventor(s):  
Gardner et al.


§ Examiner: Matthew E. Warren  
§ Group/Art Unit: 2815  
§ Customer No. 31266  
§

Title: Ultrathin High-K Gate  
Dielectric With Favorable  
Interface Properties For  
Improved Semiconductor  
Device Performance

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, M/S Issue Fee, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Name of Registered Representative

 May 3, 2005  
Signature Date

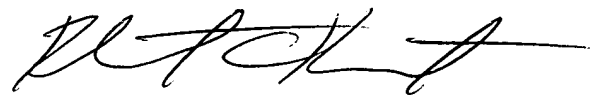
**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings be accepted for filing.

Respectfully submitted,



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